

# NC7SZ86 TinyLogic<sup>®</sup> UHS Two-Input Exclusive-OR Gate

#### **Features**

- Ultra-High Speed: t<sub>PD</sub> 2.9ns (Typical) into 50pF at 5V V<sub>CC</sub>
- High Output Drive: ±24mA at 3V V<sub>CC</sub>
- Broad V<sub>CC</sub> Operating Range: 1.65V to 5.5V
- Matches Performance of LCX Operated at 3.3V V<sub>CC</sub>
- Pow er Dow n High-Impedance Inputs/Outputs
- Over-Voltage Tolerance inputs facilitate 5V to 3V Translation
- Proprietary Noise/EMI Reduction Circuitry
- Ultra-Small MicroPak™ Packages
- Space-Saving SOT23 and SC70 Packages

#### **Description**

The NC7SZ86 is a single two-input exclusive-OR gate from ON Semiconductor's Ultra-High Speed (UHS) series of TinyLogic®. The device is fabricated with advanced CMOS technology to achieve ultra-high speed with high output drive while maintaining low static power dissipation over a broad  $V_{\rm CC}$  operating range. The device is specified to operate over the 1.65V to 5.5V  $V_{\rm CC}$  operating range. The inputs and output are high impedance when  $V_{\rm CC}$  is 0V. Inputs tolerate voltages up to 6V, independent of  $V_{\rm CC}$  operating voltage.

# Ordering Information

Part Number	Top Mark	© Eco Status	Package	Packing Method
NC7SZ86M5X	7Z86	RoHS	5-Lead SOT23, JEDEC MO-178 1.6mm	3000 Units on Tape & Reel
NC7SZ86P5X	Z86	RoHS	5-Lead SC70, EIAJ SC-88a, 1.25mm Wide	3000 Units on Tape & Reel
NC7SZ86L6X	В3	RoHS	6-Lead MicroPak™, 1.00mm Wide	5000 Units on Tape & Reel
NC7SZ86FHX	В3	Green	6-Lead, MicroPak2, 1x1mm Body, .35mm Pitch	5000 Units on Tape & Reel

# **Connection Diagrams**

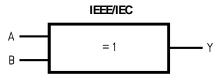
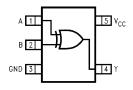


Figure 1. Logic Symbol

# **Pin Configurations**



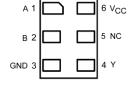


Figure 2. SC70 and SOT23 (Top View)

Figure 3. MicroPak™ (Top Through View)

#### **Pin Definitions**

Pin # SC70 / SOT23	Pin # MicroPak	Name	Description
1	1	А	Input
2	2	В	Input
3	3	GND	Ground
4	4	Υ	Output
5	6	V <sub>CC</sub>	Supply Voltage
	5	NC	No Connect

#### **Function Table**

Y=A+B

Inp	outs	Output
Α	В	Y
L	L	L
L	Н	Н
Н	L	Н
Н	Н	L

H = HIGH Logic Level

L = LOW Logic Level

### **Absolute Maximum Ratings**

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only.

Symbol	Parameter			Max.	Unit
V <sub>CC</sub>	Supply Voltage		-0.5	6.0	V
V <sub>IN</sub>	DC Input Voltage		-0.5	6.0	V
V <sub>OUT</sub>	DC Output Voltage		-0.5	6.0	V
1	DC Input Diada Current	V <sub>IN</sub> < -0.5V		-50	mΛ
l <sub>IK</sub>	DC Input Diode Current	V <sub>IN</sub> > 6.0V		+20	- mA
ı	DC Output Diada Current	V <sub>OUT</sub> < -0.5V		-50	mΛ
l <sub>ок</sub>	DC Output Diode Current	$V_{OUT} > 6V, V_{CC} = GND$		+20	- mA
l <sub>OUT</sub>	DC Output Current	•		±50	mA
I <sub>CC</sub> or I <sub>GND</sub>	DC V <sub>CC</sub> or Ground Current			±50	mA
T <sub>STG</sub>	Storage Temperature Range		-65	+150	°C
TJ	Junction Temperature Under Bia	as		+150	°C
T <sub>L</sub>	Junction Lead Temperature (So	ldering, 10 Seconds)		+260	°C
		SOT-23		200	
D	Dow or Discipation at 1959C	SC70-5		150	\^/
$P_{D}$	Pow er Dissipation at +85°C	MicroPak-6		130	- mVV
	MicroPak2-6			120	]
ESD	Human Body Model, JEDEC:JESD22-A114			4000	V
E9D	Charge Device Model: JEDEC:JE	ESD22-C101		2000	]

## **Recommended Operating Conditions**

The Recommended Operating Conditions table defines the conditions for actual device operation. Recommended operating conditions are specified to ensure optimal performance to the datasheet specifications. ON Semiconductor does not recommend exceeding them or designing to Absolute Maximum Ratings.

Symbol	Parameter	Conditions	Min.	Max.	Unit
V	Supply Voltage Operating		1.65	5.50	V
V <sub>cc</sub>	Supply Voltage Data Retention		1.50	5.50	]
$V_{IN}$	Input Voltage		0	5.5	V
V <sub>OUT</sub>	Output Voltage		0	V <sub>cc</sub>	V
T <sub>A</sub>	Operating Temperature		-40	+85	°C
		V <sub>CC</sub> =1.8V, 2.5V ± 0.2V	0	20	
t <sub>r</sub> , t <sub>f</sub>	Input Rise and Fall Times	$V_{CC} = 3.3 V \pm 0.3 V$	0	10	ns/V
		$V_{CC}$ =5.0V ± 0.5V	0	5	
$\theta_{JA}$	Thermal Resistance	SOT-23		300	°C/W

SC70-5	425	
MicroPak-6	500	
MicroPak2-6	560	

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1. Unused inputs must be held HIGH or LOW. They may not float.

## **DC Electrical Characteristics**

0	Danama (a.e.	.,	O a sa diti a sa a	Т	<sub>A</sub> =+25°	.C	T <sub>A</sub> =-40	to +85°C	I be it e
Symbol	Parameter	V <sub>cc</sub>	Conditions	Min.	Тур.	Max.	Min.	Max.	Units
	HIGH Level	1.65 to 1.95		0.75V <sub>CC</sub>			0.75V <sub>CC</sub>		.,
$V_{IH}$	Input Voltage	2.30 to 5.50		0.70V <sub>CC</sub>			0.70V <sub>CC</sub>		V
.,	LOW Level Input	1.65 to 1.95				0.25V <sub>CC</sub>		0.25V <sub>CC</sub>	V
$V_{IL}$	Voltage	2.30 to 5.50				0.30V <sub>CC</sub>		0.30V <sub>CC</sub>	V
		1.65		1.55	1.65		1.55		
		1.80	]	1.70	1.80		1.70		
		2.30	V <sub>IN</sub> =V <sub>IH</sub> , V <sub>IL</sub> I <sub>OH</sub> =-100μΑ	2.20	2.30		2.20		
		3.00	1011	2.90	3.00		2.90		
	HIGH Level	4.50	$I_{OH}$ =-4mA $I_{OH}$ =-8mA $I_{OH}$ =-16mA $I_{OH}$ =-24mA	4.40	4.50		4.40		V
$V_{OH}$	Output Voltage	1.65		1.29	1.52		1.29		V
		2.30		1.90	2.15		1.90		
		3.00		2.40	2.80		2.40		
		3.00		2.30	2.68		2.30		
		4.50	I <sub>OH</sub> =-32mA	3.80	4.20		3.80		
		1.65			0.00	0.10		0.10	
		1.80			0.00	0.10		0.10	
		2.30	V <sub>IN</sub> =V <sub>IH</sub> , or V <sub>IL</sub> I <sub>OL</sub> =100µA		0.00	0.10		0.10	
		3.00	10L 100 pr		0.00	0.10		0.10	
	LOW Level	4.50			0.00	0.10		0.10	\
$V_{OL}$	Output Voltage	1.65	I <sub>OL</sub> =4mA		0.80	0.24		0.24	V
		2.30	I <sub>OL</sub> =8mA		0.10	0.30		0.30	
		3.00	I <sub>OL</sub> =16mA		0.15	0.40		0.40	
		3.00	I <sub>OL</sub> =24mA		0.22	0.55		0.55	
		4.50	I <sub>OL</sub> =32mA		0.22	0.55		0.55	
I <sub>IN</sub>	Input Leakage Current	0 to 5.5	V <sub>IN</sub> =5.5V, GND			±1		±10	μΑ
I <sub>OFF</sub>	Power Off Leakage Current	0	V <sub>IN</sub> or V <sub>OUT</sub> =5.5V			1		10	μΑ
Icc	Quiescent Supply Current	1.65 to 5.50	V <sub>IN</sub> =5.5V, GND			2		20	μΑ

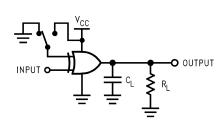
# **AC Electrical Characteristics**

Symbol	mbol Parameter V <sub>cc</sub>	V	Conditions	Т	<sub>A</sub> =+25°(	<b>C</b>	T <sub>A</sub> =-40 t	:o +85°C	Units	Figure
Symbol		V cc	Conditions -	Min.	Тур.	Max.	Min.	Max.	Units	rigure
		1.65		2.0	6.9	13.8	2.0	14.5		
t <sub>PLH</sub> , t <sub>PHL</sub>	Propagation Delay	1.80	$C_L=15pF$ , $R_I=1M\Omega$	2.0	5.7	11.5	2.0	12.0	ns	Figure 4 Figure 5
		2.50 ± 0.20		0.8	3.8	8.0	8.0	8.5		3

		$3.30 \pm 0.30$		0.5	3.0	5.7	0.5	6.0		
		$5.00 \pm 0.50$		0.5	2.4	5.0	0.5	5.4		
		$3.30 \pm 0.30$	C <sub>L</sub> =50pF,	1.5	3.5	6.2	1.5	6.5		
		5.00 ± 0.50	R <sub>L</sub> =500Ω	0.8	2.9	5.4	1.0	5.8		
C <sub>IN</sub>	Input Capacitance	0.00			4				pF	
	Power Dissipation	3.30			25				n.E	Figure 6
$C_{PD}$	Capacitance <sup>(2)</sup>	5.00			31				pF	rigule 6

#### Note:

2.  $C_{PD}$  is defined as the value of the internal equivalent capacitance which is derived from dynamic operating current consumption ( $I_{CCD}$ ) at no output loading and operating at 50% duty cycle.  $C_{PD}$  is related to  $I_{CCD}$  dynamic operating current by the expression:  $I_{CCD}=(C_{PD})(V_{CC})(f_{IN})+(I_{CC})$  static).



#### Note:

3.  $C_L$  includes load and stray capacitance. Input PRR=10MHz  $t_w$ =500ns.

Out of Phase OUTPUT

In Phase OUTPUT

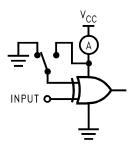
Out of Phase OUTPUT

Out of Phase OUTPUT

Vol

Figure 5. AC Waveforms

#### Figure 4. AC Test Circuit



#### Note:

4. Input=AC Waveform; t<sub>i</sub>=t<sub>i</sub>=1.8ns; PRR=10MHz; Duty Cycle=50%

Figure 6. I<sub>CCD</sub> Test Circuit

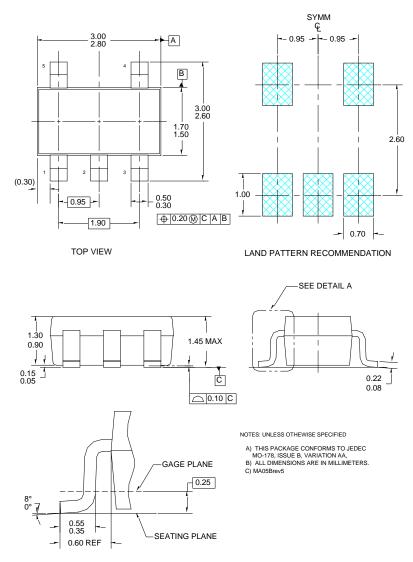
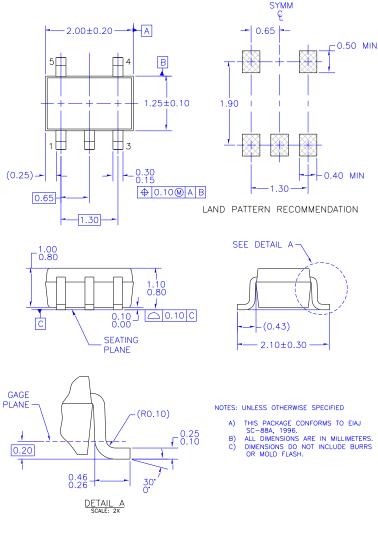


Figure 7. 5-Lead SOT23, JEDEC MO-178 1.6mm

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Package Designator	Tape Section	Cavity Number	Cavity Status	Cover Type Status
	Leader (Start End)	125 (Typical)	Empty	Sealed
M5X	Carrier	3000	Filled	Sealed
	Trailer (Hub End)	75 (Typical)	Empty	Sealed

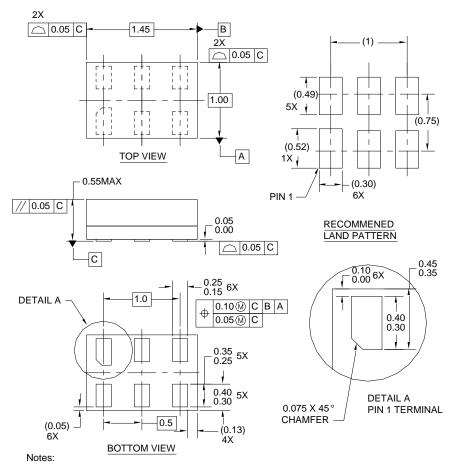


MAA05AREV5

Figure 8. 5-Lead, SC70, EIAJ SC-88a, 1.25mm Wide

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Package Designator	Tape Section	Cavity Number	Cavity Status	Cover Type Status
	Leader (Start End)	125 (Typical)	Empty	Sealed
P5X	Carrier	3000	Filled	Sealed
	Trailer (Hub End)	75 (Typical)	Empty	Sealed



- 1. CONFORMS TO JEDEC STANDARD M0-252 VARIATION UAAD
- 2. DIMENSIONS ARE IN MILLIMETERS
- 3. DRAWING CONFORMS TO ASME Y14.5M-1994

MAC06AREVC

Figure 9. 6-Lead, MicroPak™, 1.0mm Wide

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Package Designator	Tape Section	Cavity Number	Cavity Status	Cover Type Status
L6X	Leader (Start End)	125 (Typical)	Empty	Sealed
	Carrier	5000	Filled	Sealed
	Trailer (Hub End)	75 (Typical)	Empty	Sealed

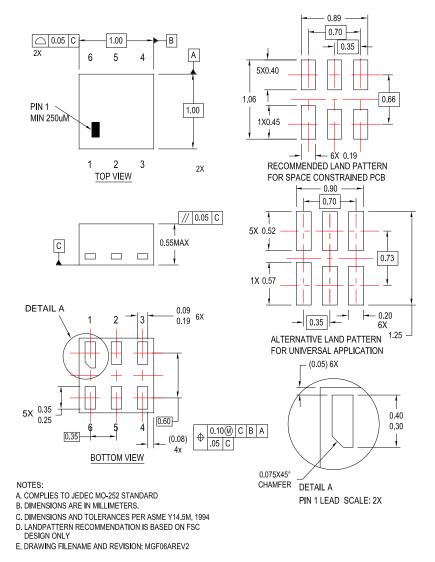


Figure 10. 6-Lead, MicroPak2, 1x1mm Body, .35mm Pitch

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FHX	Leader (Start End)	125 (Typical)	Empty	Sealed
	Carrier	5000	Filled	Sealed
	Trailer (Hub End)	75 (Typical)	Empty	Sealed

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